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(54) ROM image processing when supply of electric power is interrupted

(57) Disclosed is a method of processing a ROM image when the supply of electric power to a laser printer is interrupted during the downloading of the ROM image to a flash memory. According to this method, if the ROM

image is abnormally downloaded to the flash memory, the ROM image can be re-loaded to the flash memory. Accordingly, although the ROM image is abnormally loaded into the flash memory in the laser printer, it is possible to use the laser printer as normal.

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Description

BACKGROUND TO THE INVENTION

[0001] The present invention relates to a method of processing a ROM image when the supply of electric power to a laser printer is interrupted.

[0002] Generally, a laser printer performs accurate operations and communicates with a computer. The laser printer has various functions, for example emulation modes such as KSSM, PCL, and the like. Thus, the laser printer has a separate microprocessor to perform printing according to a BIOS which is stored in a ROM. In general, the BIOS is stored in a once-programmable ROM or an erasable and programmable ROM (EPROM). The ROM in which the BIOS is stored is not damaged so long as physical impact is not sustained and the data stored in it are preserved even if the supply of electric power is interrupted.

[0003] As described above, according to the present invention, the ROM image can be freely read from the ROM and written onto a flash memory, preserving the recorded data even if the supply of electric power is interrupted. If bugs which are found in the BIOS are corrected or the BIOS is upgraded after the production and sale of the laser printer, the user can obtain the corrected BIOS or the upgraded BIOS from a manufacturer through a communications line or an Internet site so as to change the BIOS having the bug or the old version of the BIOS to the corrected BIOS or the new version of the BIOS. Accordingly, product reliability can be increased.

[0004] It has been proposed to download a ROM image to a flash memory of a laser printer. The printer is provided with a RAM and the flash memory. The laser printer has the ROM image which is downloaded from a computer through a parallel port and stores the ROM image in the RAM. When a header is searched for and found in the ROM image, an ID field is read from the header. A flash memory changing table is set for a sector to be changed according to the ID field. Then, recorded data are deleted from the sector of the flash memory and the ROM image which is stored in the RAM is copied to the sector of the flash memory. However, this method of downloading the ROM image to the flash memory of the laser printer only works when electric power is normally supplied for the laser printer.

[0005] If, while the program which is recorded in the BIOS area of the flash memory is being deleted so that the laser printer can download the ROM image to its flash memory, the supply of electric power to the laser printer is interrupted, the program which is recorded in the boot area may be in part deleted. Accordingly, even though electric power is restored to the laser printer, the laser printer cannot be operated because the program in the boot area cannot be performed due to its partial deletion, in spite of there being a jump code in the boot area to the BIOS.

[0006] If the supply of electric power to the laser printer is interrupted before the BIOS ROM image in the RAM is copied to the flash memory, after the program which is recorded in the BIOS area of the flash memory is deleted, even though electric power is restored to the laser printer, the laser printer cannot be operated because the data which are recorded in the BIOS area have been deleted, in spite of there being a jump code in the boot area to the BIOS.

[0007] Moreover, if the supply of electric power to the laser printer is interrupted during copying of the BIOS ROM image from the RAM to the BIOS area of the flash memory after the program which is recorded in the BIOS area of the flash memory is deleted, even though electric power is restored to the laser printer, the laser printer cannot be normally operated because the ROM image is incompletely downloaded to the BIOS area, in spite of there being a jump code in the boot area to the BIOS.

SUMMARY OF THE INVENTION

[0008] The present invention is designed to address these difficulties. It is an object of the present invention to provide a method of processing a ROM image when the supply of electric power to a laser printer is interrupted during down-loading of the ROM image, in which even if the ROM image is abnormally loaded to the flash memory due to the interruption of the supply of electric power, the ROM image may be corrected so that the laser printer can be normally operated.

[0009] Accordingly, the present invention provides a method of processing a ROM image when the supply of electric power to a computer peripheral is interrupted, comprising:

checking whether or not the ROM image is normally recorded in each of a plurality of program regions of a flash memory of the computer peripheral;
setting the computer peripheral to a normal mode when it is determined that the ROM image is normally recorded in each program region;
setting the computer peripheral to a down-load mode when it is determined that the ROM image is abnormally recorded in one of more program regions; and
when the computer peripheral is set to the down-load mode, reloading the ROM image into at least the said one or more program regions of the flash memory.

[0010] Checking whether or not the ROM image is normally recorded in each program region may comprise detecting a check word in that program region. The check words may be recorded at the beginning and the end of the program region.

[0011] Preferably, checking whether or not the ROM image is normally recorded in each program region includes determining the state of the ROM image from the

following:

- a first state in which the program region is partially deleted;
- a second state in which the program region is fully deleted and not re-written;
- a third state in which the program region is partially re-written; and
- a fourth state.

[0012] The ROM image may be re-loaded by:

- down-loading the ROM image through a parallel port to a RAM;
- determining whether or not a header is included in the ROM image stored in the RAM;
- reading an ID of the header when the ROM image which is stored in the RAM includes such a header;
- setting a table of changes for the flash memory according to the ID;
- checking whether or not it is determined that the header is included in all regions of the ROM image stored in the RAM; and
- copying the ROM image to the flash memory according to the states of the ROM image when the detecting of the header is complete throughout all region of the ROM image stored in the RAM.

[0013] Preferably, when the ROM image of one or more program regions is in the first state, the ROM image stored in that or those regions is deleted and the corresponding region or regions of the ROM image stored in the RAM is copied to the flash memory. Preferably, when the ROM image of one or more program regions is in the second state, the corresponding region or regions of the ROM image stored in the RAM is copied to the flash memory. Preferably, when the ROM image of one or more program regions is in the third state, the ROM image stored in that or those regions of the flash memory is deleted and the ROM image which is stored in the corresponding region or regions of the RAM is copied to the flash memory.

[0014] The table of changes for the flash memory preferably has bits corresponding to sectors of the flash memory, which are respectively set to one when the corresponding sector of the flash memory is changed and to zero when it is unchanged.

[0015] The header may include a two byte header field which indicates that the ROM image is an image to be changed and a one byte of an ID field which indicates the kind of the ROM image to be changed.

[0016] Preferably, the method is performed by execution of a program stored in the boot area of the flash memory. The boot area of the flash memory may be provided with:

- a routine for detecting whether or not the ROM image is normally recorded in the program regions;

- a transmitting driver for storing the ROM image in the RAM after receiving the ROM image which is transmitted through the parallel port;
- a RAM driver for setting the table of changes for the flash memory by reading the ID after it is determined whether or not the header is presented in the RAM; and
- a flash memory driver for copying the ROM image in the RAM to the corresponding sector of the flash memory after the ROM image is deleted from the corresponding sector of the flash memory according to the table of the changes for the flash memory.

BRIEF DESCRIPTION OF THE DRAWINGS

[0017] The present invention will now be described by way of example with reference to the accompanying drawings, in which:

- FIG. 1 is a schematic block diagram showing an apparatus for loading a ROM image into a laser printer;
- FIG. 2 shows the structure of a conventional ROM map in the laser printer;
- FIG. 3 shows the boot area of the ROM map;
- FIG. 4 shows the boot area of a ROM map in a laser printer to which the present invention is applied;
- FIG. 5 shows a region starting check word and a region ending check word;
- FIG. 6 shows a header of a flash memory;
- FIG. 7 shows a table of changes for the flash memory;
- FIG. 8 is a flow chart showing processing of the ROM image according to the present invention; and
- FIG. 9 is a flow chart showing loading of the ROM image to the flash memory according to the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

[0018] FIG. 1 is a block diagram schematically showing an apparatus for loading the ROM image into the laser printer which has a flash memory. As shown in FIG. 1, the apparatus for loading the ROM image into the laser printer includes a microprocessor 1 for controlling the system generally according to instructions in a program which is stored in the flash memory, the flash memory 2 having the ROM image, such as BIOS and emulation software stored in it to operate the microprocessor 1, a parallel port 4 through which the ROM image is loaded from a computer to the flash memory of the laser printer under the control of the microprocessor 1, and a RAM 3 in which the ROM image is temporarily stored.

[0019] As shown in FIG. 2, the typical ROM in the laser printer has address areas which are arranged from a lower portion to an upper portion in FIG. 2 and includes

a boot area 6 which is firstly performed when electric power is supplied to the laser printer and a program area 7 having a BIOS to control the overall operation of the laser printer and an emulation mode program for each emulation mode of the laser printer.

[0020] As shown in FIG. 3, the ROM includes a boot area 6 which has a jump code 8 and a down load program 9 for loading the ROM image to the flash memory, and a program area 7 which has a BIOS 10 for controlling an overall operation of the laser printer and an emulation mode program for each emulation mode for the laser printer. As shown in FIG. 4, the ROM of the present invention includes the same areas as that of the ROM in FIG. 3, except that its boot area has a detecting routine 11.

[0021] With reference to FIG. 3, when electric power is supplied to the laser printer, the microprocessor 1 reads a jump code 8 in the boot area 6 so as to determine whether the laser printer is presented either in a normal mode or in a down-loading mode. If the laser printer is in the normal mode, the microprocessor 1 reads the BIOS in the program region, while if the laser printer is in the down-loading mode, the microprocessor 1 reads a down-loading program in the boot area 6. On the other hand, a user may set a desired mode by pushing a specified button on the laser printer.

[0022] With reference to FIG. 4, according to the present invention, the detecting routine 11 is added to the flash memory before the sector of the jump code 8. The microprocessor 1 determines whether the ROM BIOS and the emulation programs are normally recorded on each program area 7 in the flash memory 2 so as to set the operation mode of the laser printer.

[0023] FIG. 5 shows a region starting check word and a region ending check word which are applied to the ROM image according to the present invention. As shown in FIG. 5, the check words 12 and 13 are each two bytes at the start and end of each program area 7 in which the ROM image is down-loaded. The check words 12 and 13 include an area starting check word 12 indicating the start of each program area 7 and an area ending check word 13 indicating the end of each program area 7. Each of the check words 12 and 13 has a specified value. Each of the check words 12 and 13 described will be limited to 55AA hexadecimal to help with understanding the present invention. Moreover, if the check words 12 and 13 are deleted from the flash memory 2, they have a value of FFFF hexadecimal.

[0024] As shown in FIG. 8, when electric power is supplied to the laser printer, the detecting routine 11 in the boot area 6 is performed. The detecting routine 11 detects the check words 12 and 13 throughout the program area 7 including the BIOS and the emulation programs in the flash memory 2 in step S1. Then, the detecting routine 11 checks whether the BIOS and the emulation programs are correctly recorded on the program area 7 in step S2.

[0025] When it is determined that the ROM image is

normally recorded on all the areas of the flash memory 2, the laser printer is set into the normal mode in step S5 and the microprocessor 1 reads the BIOS 10. On the other hand, when it is determined that the ROM image is abnormally recorded in the flash memory 2, the laser printer is set to a down-loading mode in step S3, and down-loading of the ROM image is performed again in step 4.

[0026] FIG. 9 is a flow chart showing steps of loading the ROM image to the flash memory according to the present invention. As shown in FIG. 2, the microprocessor 1 receives the ROM image from the computer 5 through the parallel port 4 by using the down loading program in the boot area 6 in step S6. The ROM image is stored in the RAM 3 by the flash memory driver in step S7.

[0027] As shown in FIG. 6, the ROM image which is down-loaded into the RAM 3 has a header 14 to the extent of one sector of the flash memory 2. The header 14 includes a header field 15 which has two byte in size and indicates the image to be changed and an ID field 16 which is two bytes in size and indicates the type of the ROM image.

[0028] In step S8, it is checked whether a header 14 is detected which has a size corresponding to the one sector of the flash memory 2 after the ROM image is down-loaded into the RAM 3. In step S9, when the header 9 is detected in step S8, the ID field 16 which is positioned after the header field 15 is read in step S9.

[0029] The ID field 16 indicates whether the ROM image is either the BIOS image or any one image of the plurality of the emulation images. According to the ID field 16, the down load program in the boot area 6 sets a bit of the sector to be changed into one bit in the flash memory changing table 17 as shown in FIG. 7, in step S10. Then, in step S11, the down load program checks whether it has completed the detection of the header 14 in all regions of the RAM 3. If the detecting of the header 15 is incomplete, steps S8 to S11 are again performed. If the detecting of the header 14 in all regions of the RAM 3 is complete, the image which is down-loaded in the RAM is copied down to the flash memory 2 according to the flash memory changing table 17.

[0030] The region of the program which has errors may have one of three states in step S1. The copying operation according to the three states of the program region is as follows. The first state is when the region ending check word 13 is 55AA and the region starting check word 12 is FFFF in the program region 7 which is detected, indicating that the supply of electric power was interrupted in the deletion step during the down-loading of the ROM image to the program region. The ROM image of the RAM 3 is copied to the program region after deleting the data in the program region, in step 14.

[0031] The second state is when the region starting check word 12 and the region ending check word 13 are FFFF, indicating that the supply of electric power was interrupted before the step of copying the ROM image

in the RAM to the flash memory and after step of deleting the recorded data during the down-loading of the ROM image in the RAM to the flash memory. It is unnecessary to delete the program region. Accordingly, the ROM image in the RAM 3 is copied to the program region in step S15. 5

[0032] The third state is when the region starting check word 12 is 55AA, but the region ending check word 13 is FFFF, indicating that the supply of electric power was interrupted during the step of copying the ROM image to the flash memory but after deleting the recorded data in the program region during the down-loading of the ROM image to the program region. Therefore, the ROM image in the RAM 3 is copied to the program region in step S17 after the recorded data is again deleted in step S16. 10 15

[0033] Even though the ROM image may be abnormally down-loaded to the flash memory 2 because the supply of electric power to the laser printer was interrupted during the down-loading of the ROM image to the flash memory 2, the ROM image can be recorded in the flash memory 2 according to the present invention. 20

[0034] In this method of processing the ROM image when the supply of electric power is interrupted, since the detecting routine is added to the boot area of the flash memory to detect whether the ROM image is normally recorded in the flash memory 2, the ROM image is reloaded to the flash memory 2 according to the determination of the detecting routine. Accordingly, malfunctions of the laser printer can be prevented. 25 30

[0035] Furthermore, since the method of processing the ROM image is provided even though the supply of electric power is interrupted during the down-loading of the ROM image to the flash memory of the laser printer, product reliability can be improved and the cost of after-sales service can be reduced. 35

[0036] While the present invention has been particularly shown and described with reference to a laser printer, it is equally applicable to a range of computer peripherals. 40

Claims

1. A method of processing a ROM image when the supply of electric power to a computer peripheral is interrupted, comprising: 45

checking whether or not the ROM image is normally recorded in each of a plurality of program regions of a flash memory of the computer peripheral; 50
 setting the computer peripheral to a normal mode when it is determined that the ROM image is normally recorded in each program region; 55
 setting the computer peripheral to a down-load mode when it is determined that the ROM im-

age is abnormally recorded in one of more program regions; and
 when the computer peripheral is set to the down-load mode, reloading the ROM image into at least the said one or more program regions of the flash memory.

2. A method according to claim 1 in which checking whether or not the ROM image is normally recorded in each program region comprises detecting a check word in that program region.

3. A method according to claim 2 in which the check words are recorded at the beginning and the end of the program region.

4. A method according to any preceding claim in which checking whether or not the ROM image is normally recorded in each program region includes determining the state of the ROM image from the following:
 - a first state in which the program region is partially deleted;
 - a second state in which the program region is fully deleted and not re-written;
 - a third state in which the program region is partially re-written; and
 - a fourth state.

5. A method according to claim 4 in which the ROM image is re-loading by:
 - down-loading the ROM image through a parallel port to a RAM;
 - determining whether or not a header is included in the ROM image stored in the RAM;
 - reading an ID of the header when the ROM image which is stored in the RAM includes such a header;
 - setting a table of changes for the flash memory according to the ID;
 - checking whether or not it is determined that the header is included in all regions of the ROM image stored in the RAM; and
 - copying the ROM image to the flash memory according to the states of the ROM image when the detecting of the header is complete throughout all region of the ROM image stored in the RAM.

6. A method as claimed in claim 5 in which when the ROM image of one or more program regions is in the first state, the ROM image stored in that or those regions is deleted and the corresponding region or regions of the ROM image stored in the RAM is copied to the flash memory.

7. A method as claimed in claim 5 or claim 6 in which when the ROM image of one or more program regions is in the second state, the corresponding region or regions of the ROM image stored in the RAM is copied to the flash memory. 5

8. A method as claimed in any one of claims 5-7 in which when the ROM image of one or more program regions is in the third state, the ROM image stored in that or those regions of the flash memory is deleted and the ROM image which is stored in the corresponding region or regions of the RAM is copied to the flash memory. 10

9. A method as claimed in any one of claims 5-8 in which the table of changes for the flash memory has bits corresponding to sectors of the flash memory, which are respectively set to one when the corresponding sector of the flash memory is changed and to zero when it is unchanged. 15
20

10. A method as claimed in any one of claims 5-9 in which the header includes a two byte header field which indicates that the ROM image is an image to be changed and a one byte of an ID field which indicates the kind of the ROM image to be changed. 25

11. A method as claimed in any preceding claim which is performed by execution of a program stored in the boot area of the flash memory. 30

12. A method as claimed in claim 5 which is performed by execution of a program stored in the boot area of the flash memory, the boot area being provided with: 35
 - a routine for detecting whether or not the ROM image is normally recorded in the program regions;
 - a transmitting driver for storing the ROM image in the RAM after receiving the ROM image which is transmitted through the parallel port; 40
 - a RAM driver for setting the table of changes for the flash memory by reading the ID after it is determined whether or not the header is presented in the RAM; and 45
 - a flash memory driver for copying the ROM image in the RAM to the corresponding sector of the flash memory after the ROM image is deleted from the corresponding sector of the flash memory according to the table of the changes for the flash memory. 50

13. A method of processing a ROM image when the supply of electric power to a computer peripheral is interrupted, as described herein with reference to and as illustrated in FIGs. 5-9 of the accompanying drawings. 55

FIG. 1

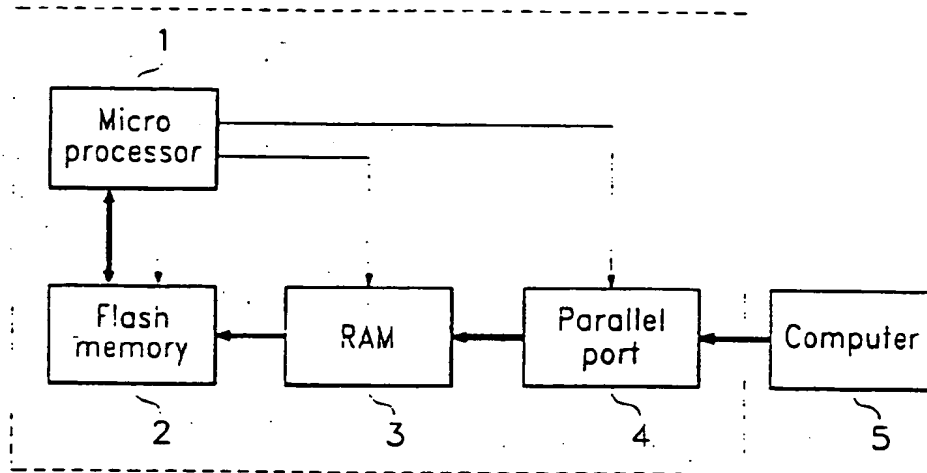


FIG. 2

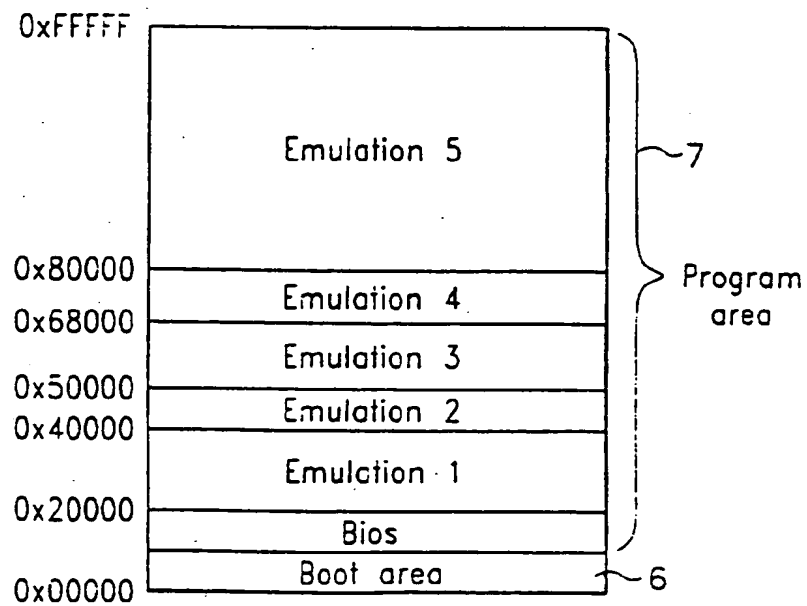


FIG. 3

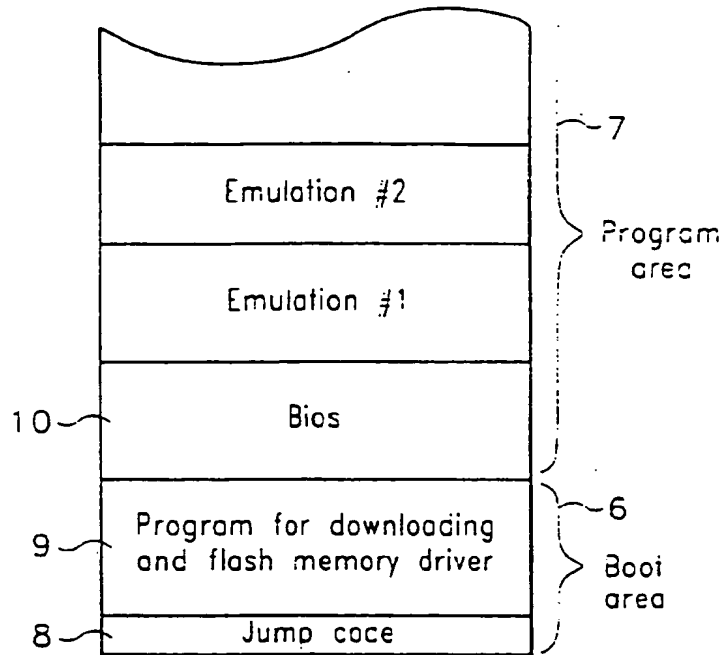


FIG. 4

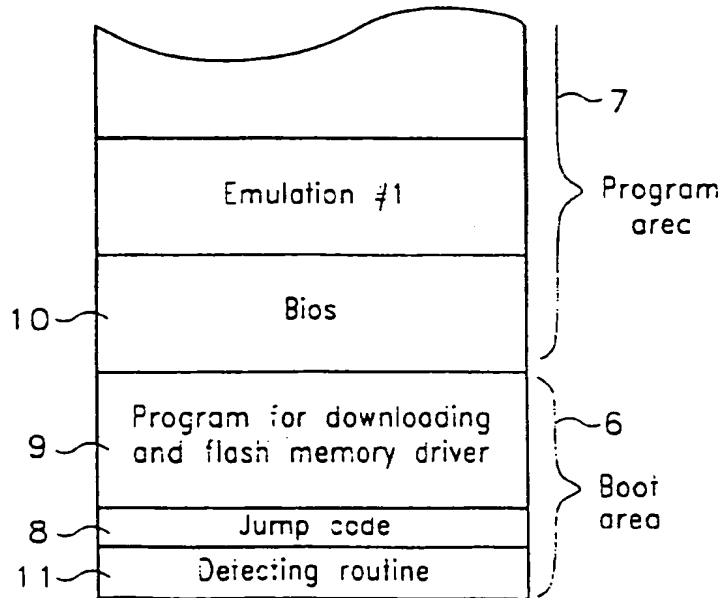


FIG. 5

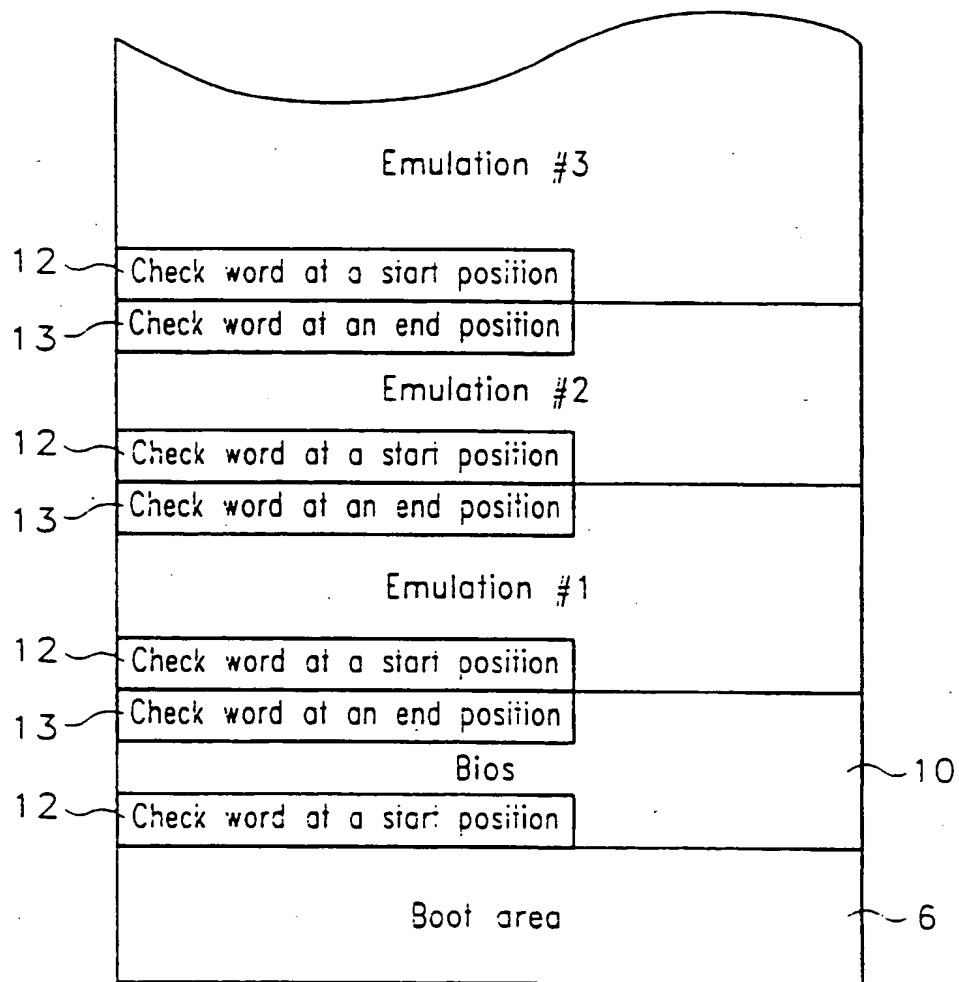


FIG. 6

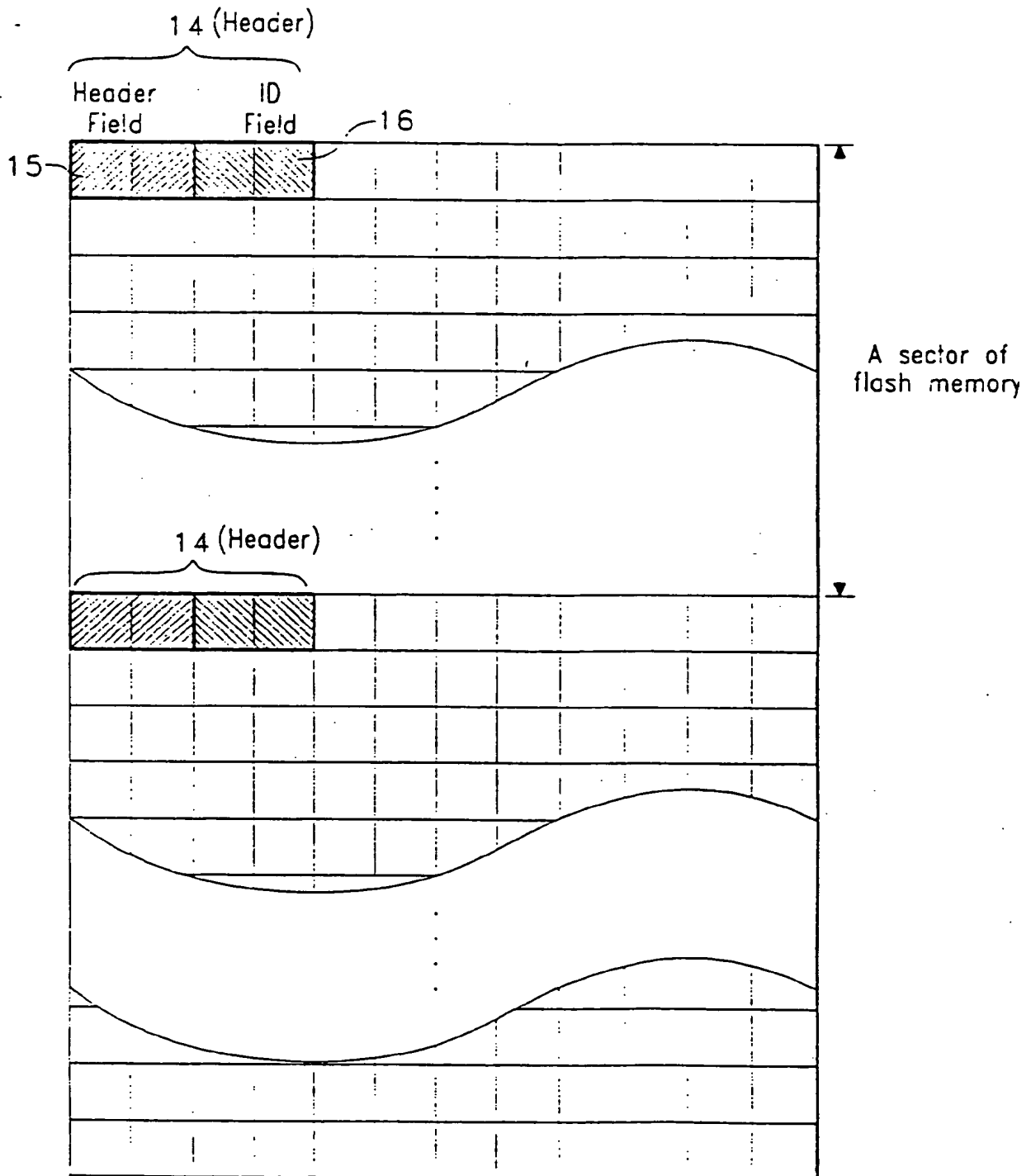


FIG. 7

Sector numbers	0	1	2	3	4	5	6	7	8
It is determined whether or not a change is presented	NON-ERASABLE	0	0	0	1	0	0	0	1
									17

FIG. 8

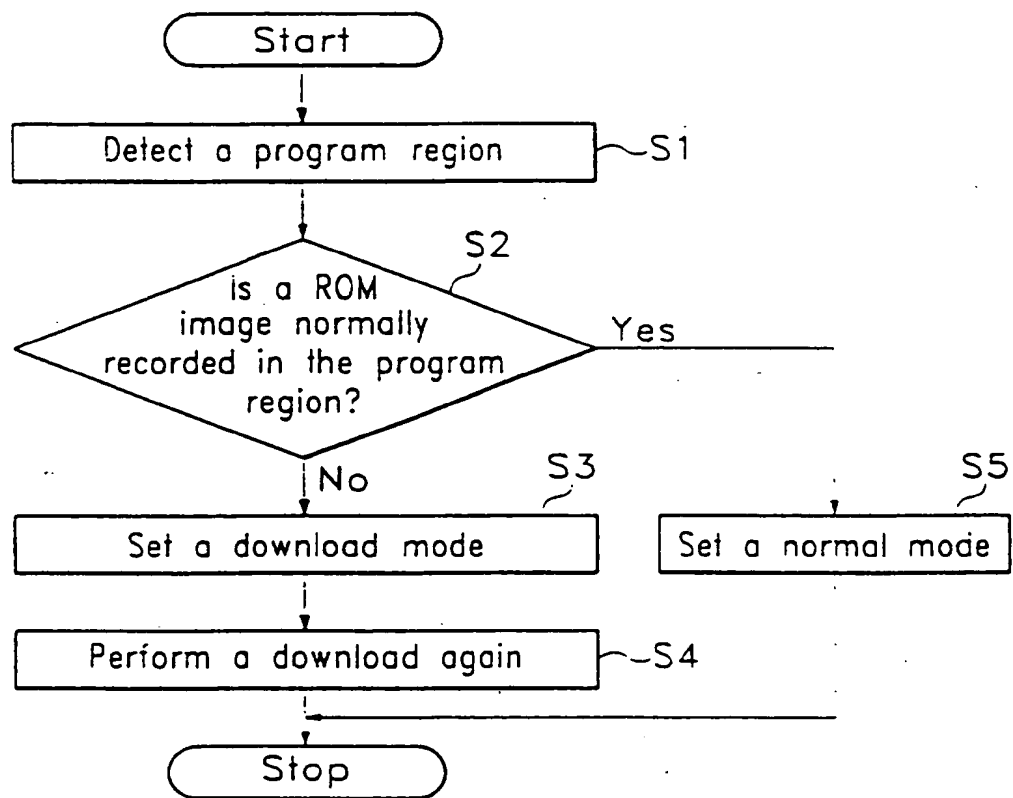
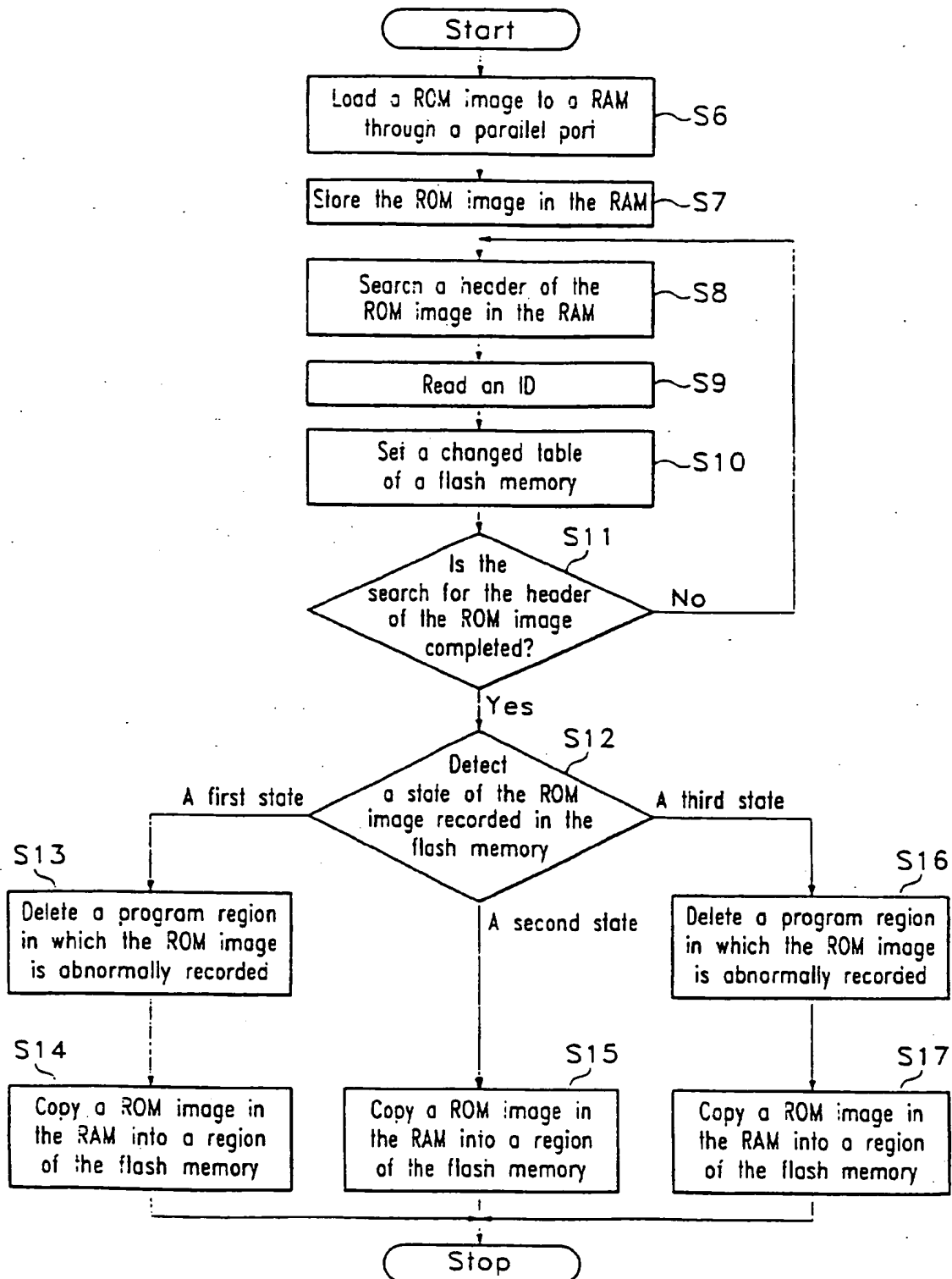
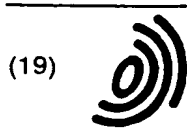


FIG. 9





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(54) **ROM image processing when supply of electric power is interrupted**

(57) Disclosed is a method of processing a ROM image when the supply of electric power to a laser printer is interrupted during the downloading of the ROM image to a flash memory. According to this method, if the ROM

image is abnormally downloaded to the flash memory, the ROM image can be re-loaded to the flash memory. Accordingly, although the ROM image is abnormally loaded into the flash memory in the laser printer, it is possible to use the laser printer as normal.

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EUROPEAN SEARCH REPORT

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EP 98 30 9117

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A		5, 10, 11	
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A	ANONYMOUS: "Simple Data Integrity Method for Spanned, Segmented Data." IBM TECHNICAL DISCLOSURE BULLETIN, vol. 34, no. 12, 1 May 1992 (1992-05-01), pages 295-296, XP000308520 New York, US * the whole document *	2, 3	TECHNICAL FIELDS SEARCHED (Int. Cl. 6)
A	"Adapter Microcode Protection during Download" IBM TECHNICAL DISCLOSURE BULLETIN., vol. 37, no. 10, October 1994 (1994-10), pages 181-185, XP000475624 IBM CORP. NEW YORK., US ISSN: 0018-8689 * the whole document *	1, 2, 5, 10, 11	G06F
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The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	13 October 2000	Herreman, G	
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
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The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 13 October 2000	Examiner Herreman, G
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone</p> <p>Y : particularly relevant if combined with another document of the same category</p> <p>A : technological background</p> <p>O : non-written disclosure</p> <p>P : intermediate document</p> <p>T : theory or principle underlying the invention</p> <p>E : earlier patent document, but published on, or after the filing date</p> <p>D : document cited in the application</p> <p>L : document cited for other reasons</p> <p>& : member of the same patent family, corresponding document</p>			

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